# **GRACE**

## **SPECIFICATION**

**ROHS** Compliant Parts

Customer :	
Part Name :	Chip PTC Thermistor
Part Number:	KPTC-S Series

## Dongguan GRACE electronic Technology Co., LTD

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## Chip PTC Thermistor — KPTC - S series

For Overheat Sensing



#### Features

- Suitable for miniaturizing circuits due to small size SMD type
- 100% Pb free, RoHS

## Applications

For overheating detection in FET, power IC, and other heating areas.

### Part Numbering

KPTC	0603	S	471	L	135	s	X	XXXX	Т
1	2	3	4	6	6	Ø	8	9	100

1	Series
00.00	
	GRACE
Chip CPTC Thermistor	

2	Chip size (EIA)
	0402
	0603
	0805

3	Series code
Р	Over Current Protection
s	Overheat Sensing

4	Nominal resistance $R_{25}(\Omega)$
471	470
102	1,000
103	10,000

<b>⑤</b>	Resistance tolerance
М	±20%
N	±30%
L	±50%

6	Sensing Temperature (℃)
075	75
135	135

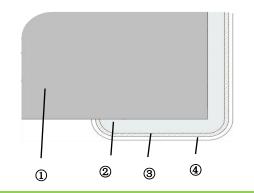
Ø	Sensing Temperature (℃)
s	Single sensing product
D	Dual sensing product

8	internal code
	x

9	Customer identification code	
	XXXX	

100	Packaging style
т	Таре
В	Bulk

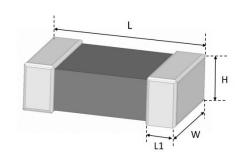
### Construction



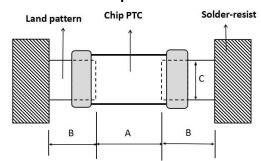
No.	Name					
1	PTC Semiconductive	PTC Semiconductive Ceramics				
2		Ag				
3	Terminal electrode	Ni				
4		Sn				

## Shape and Dimensions

### 1) Dimensions:



## 2) Recommended PCB pattern for reflow soldering:



Unit: mm

Size (EIA/JIS)	L	W	Н	L1	A	В	C
0402/1005	$1.00 \pm 0.20$	$0.50 \pm 0.20$	$0.50 \pm 0.20$	0.30±0.20	0.45~0.55	0.40~0.50	0.45~0.55
0603/1608	1.60±0.20	0.80±0.20	0.80±0.20	0.30±0.20	0.60~0.80	0.60~0.80	0.60~0.80
0805/2012	2.00±0.20	1.20±0.20	0.85±0.20	0.45±0.25	0.80~1.20	0.80~1.20	0.90~1.60

## Electrical Characteristics

### Single sensing temperature 0402 Type

Part Number	Resistance @25℃	Sensing Temp.		Max. Voltage	Operating Temp.
	$R_{25}(\Omega)$	Ts (	℃)	V <sub>max</sub> (V)	T <sub>L</sub> ~T <sub>U</sub> (℃)
KPTC0402S471□095S□□T		95 ± 5			<b>-25</b> ∼ 110
KPTC0402S471□105S□□T	470	105 ± 5	@4.7h.0		<b>-25</b> ∼ <b>120</b>
KPTC0402S471□115S□□T	470	115 ± 5	- @4.7k Ω	(W4./K 54	<b>-25</b> ∼ 130
KPTC0402S471□125S□□T		125 ± 5			<b>-25</b> ∼ 140
KPTC0402S102□085S□□T	1,000	85 ± 5	0.101.0	33	-25 ~ 100
KPTC0402S102□125S□□T	1,000	125 ± 5	@10kΩ	@10k Ω 32	<b>-25</b> ∼ 140
KPTC0402S103□080S□□T		80 ± 5			<b>-25</b> ~ 95
KPTC0402S103□095S□□T	10.000	95 ± 5	047040		<b>-25</b> ∼ 110
KPTC0402S103□100S□□T	10,000	100 ± 5	@4.7M Ω		<b>-25</b> ∼ 115
KPTC0402S103□110S□□T		110 ± 5			<b>-25</b> ∼ <b>125</b>

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## **Specifications for Chip PTC Thermistor**

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KPTC0402S103□120S□□T		120 ± 5		<b>-25</b> ∼ 135
KPTC0402S103□130S□□T		130 ± 5		<b>-25</b> ∼ 145
KPTC0402S473□130S□□T	47,000	130 ± 5	@4.7M Ω	<b>-25</b> ∼ 145

## Single sensing temperature 0603 Type

Part Number	Resistance @25°C $R_{25}(\Omega)$	Sensing Ts (		Max. Voltage $V_{max}(V)$	Operating Temp. $T_{L}{\sim}T_{U}  ({}^{{}_{}}{\mathbb{C}})$
KPTC0603S101□115S□□T		115 ± 7	_		<b>-25</b> ∼ 130
KPTC0603S101□130S□□T	100	130 ± 7	<u>@</u> 1KΩ		<b>-25 ∼ 145</b>
KPTC0603S221□090S□□T	220	90 ± 7		24	<b>-25 ∼ 105</b>
KPTC0603S221□105S□□T	220	105 ± 7	- @2.2KΩ		<b>-25 ∼ 120</b>
KPTC0603S471□065S□□T		65 ± 5			<b>-25</b> ∼ <b>80</b>
KPTC0603S471□075S□□T		75 ± 5			<b>-25</b> ~ 90
KPTC0603S471□080S□□T		80 ± 5			<b>-25</b> ~ 95
KPTC0603S471□085S□□T		85 ± 5			<b>-25 ∼ 100</b>
KPTC0603S471□095S□□T		95 ± 5			<b>-25</b> ∼ 110
KPTC0603S471□100S□□T	470	100 ± 5	@4.7KΩ	32	<b>-25</b> ∼ 115
KPTC0603S471□105S□□T		105 ± 5			<b>-25 ∼ 120</b>
KPTC0603S471□110S□□T		110 ± 5			<b>-25</b> ∼ <b>125</b>
KPTC0603S471□115S□□T		115 ± 5	_		<b>-25</b> ∼ 130
KPTC0603S471□125S□□T		125 ± 5			<b>-25</b> ∼ 140
KPTC0603S471□135S□□T		135 ± 5			<b>-25</b> ∼ 150
KPTC0603S102□095S□□T		95±5			<b>-25</b> ∼ 110
KPTC0603S102□105S□□T	1,000	105±5	@4.7M Ω	32	<b>-25</b> ∼ <b>120</b>
KPTC0603S102□115S□□T		115±5			<b>-25</b> ∼ 130
KPTC0603S103□075S□□T		75±5			-25 ~ 90
KPTC0603S103□080S□□T		80±5			-25 ~ 95
KPTC0603S103□110S□□T	10,000	110±5	@4.7M Ω	32	<b>-25</b> ∼ <b>125</b>
KPTC0603S103□120S□□T		120±5			<b>-25</b> ~ 135
KPTC0603S103□130S□□T		130±5			-25 ~ 145
KPTC0603S473□130S□□T	47,000	130±5	@4.7M Ω	32	-25 ~ 145

## Single sensing temperature 0805 Type

Part Number	Resistance @25℃	Sensing Temp.  Ts (℃)		Max. Voltage	Operating Temp.
	R <sub>25</sub> (Ω)			V <sub>max</sub> (V)	$T_{L}\sim T_{U}$ (°C)
KPTC0805S150□130S□□T	15	130±7	@1KΩ	32	<b>-25</b> ∼ 145
KPTC0805S101□110S□□T		110±7			<b>-25</b> ∼ <b>125</b>
KPTC0805S101□115S□□T	100	115±7	@1KΩ	24	<b>-25</b> ∼ 130
KPTC0805S101□130S□□T		130±7			<b>-25</b> ∼ 145
KPTC0805S471□065S□□T		65±5			-25 ~ 80
KPTC0805S471□075S□□T		75±5			-25 ~ 90
KPTC0805S471□085S□□T		85±5	@4.7KΩ		<b>-25</b> ∼ 100
KPTC0805S471□090S□□T		90±5			<b>-25</b> ∼ 105
KPTC0805S471□095S□□T		95±5		32	<b>-25</b> ∼ 110
KPTC0805S471□100S□□T	470	100±5			<b>-25</b> ∼ 115
KPTC0805S471□105S□□T		105±5			<b>-25</b> ∼ <b>120</b>
KPTC0805S471□115S□□T		115±5			<b>-25</b> ∼ 130
KPTC0805S471□120S□□T		120±5			<b>-25</b> ∼ <b>135</b>
KPTC0805S471□125S□□T		125±5			<b>-25</b> ∼ 140
KPTC0805S471□135S□□T		135±5			<b>-25</b> ∼ 150
KPTC0805S103□110S□□T	10.000	110±5	047040	22	<b>-25 ∼ 125</b>
KPTC0805S103□130S□□T	10,000	130±5	@4.7M Ω	32	<b>-25</b> ~ 145

## **Dual sensing temperature 0402 Type**

Part Number	Resistance @25℃	Sensing Temp.1 Sensing Temp.2 Max. Voltage		Temp.1 Sensing Temp.2		Operating Temp.	
	$R_{25}(\Omega)$	Ts1	(°C)	Ts2 (℃)		V <sub>max</sub> (V)	T <sub>L</sub> ~T <sub>U</sub> (℃)
KPTC0402S471□115D□□T	470	115 ± 5	@4.7K.0	130 ± 7	@47K 0	32	<b>-25</b> ∼ 140
KPTC0402S541□115D□□T	540	115 ± 5	@4.7KΩ	135 ± 7	@47KΩ	32	<b>-25</b> ∼ 150
KPTC0402S102□065D□□T		65±5		80±5			-40 ~ 90
KPTC0402S102□075D□□T		75±5		90±5	@100KΩ	32	-40 ~ 100
KPTC0402S102□085D□□T		85±5		100±5			-40 ~ 110
KPTC0402S102□095D□□T	1,000	95±5	@10K Ω	0K Ω 110±5 120±3 130±5			-40 ~ 120
KPTC0402S102□105D□□T		105±5					-40 ~ 130
KPTC0402S102□115D□□T		115±5					-40 ~ 140
KPTC0402S102□125D□□T		125±5		140±5			-40 ~ 150

## **Dual sensing temperature 0603 Type**

Part Number	Resistance @25℃	Sensing Temp.1		Sensing Temp.2		Max. Voltage	Operating Temp.
	$R_{25}(\Omega)$	Ts1	(°C)	Ts2 (℃)		V <sub>max</sub> (V)	$T_{L}\sim T_{U}$ (°C)
KPTC0603S471□065D□□T		65±5		80±7			-25 ~ 90
KPTC0603S471□075D□□T		75±5		90±7			<b>-25</b> ∼ 100
KPTC0603S471□085D□□T		85±5		100±7			<b>-25</b> ~ 110
KPTC0603S471□095D□□T		95±5		110±7			-25 ~ 120
KPTC0603S471□105D□□T	470	105±5	@4.7KΩ	120±7	@47KΩ	32	<b>-25</b> ∼ 130
KPTC0603S471□115D□□T		115±5		130±7			-25 ~ 140
KPTC0603S471□125D□□T		125±5		140±7			<b>-25</b> ∼ 150
KPTC0603S471□130D□□T		130±5		145±5			<b>-25</b> ~ 155
KPTC0603S471□135D□□T		135±5		150±7			<b>-25</b> ∼ 160

**<sup>※</sup>** The above data were tested in stationary air at 25℃ with unmounted independent units.

## Description and definition of terms

No.	Items	Test Methods and Remarks
1	Nominal Zero-Power Resistance (R25)	Ambient temperature: 25±0.2℃.  Measuring electric power: 0.1mW Max.
2	Sensing Temperature	In the current voltage characteristics of PTC, the temperature when the resistance rises sharply
3	Max. Voltage	Refers to the maximum voltage that can be applied to PCT within the working temperature range

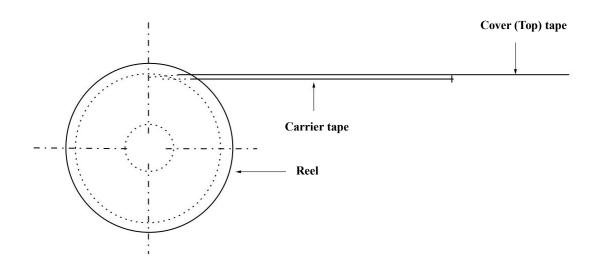
## Reliability Test

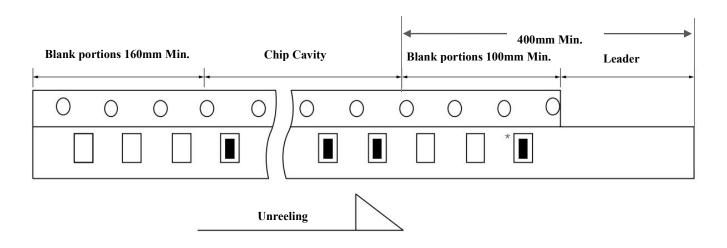
Items	Requirements			Test Methods and Remarks			
	other defects shall occur.			Solder the chip to the testing jig (glass epoxy board shown in the following Fig. 1-1) using eutectic solder. Then apply a force in the direction of the arrow.			
Terminal Strength			Size (EIA) 0402、0603 0805		orce 5N 0N	Duration 10±1s	
	Fig.1. No visible mecha	nical damaş		_			y board shown in
	Unit: n Size (EIA) a 0402 0.4 0603 1.0 0805 1.2	b 1.5 3.0 4.0	0.5 1.2 1.65	Size (EIA)  0402、0603、 0805		Pressurizi Speed <0.5mm	Duration
Resistance to Flexure	Unit: mm			R230 Flexure Fig.2-2			
Vibration	No visible mechanical damage.  Cu pad Solder mask  Glass Epoxy Board  Fig. 3-1			in Fig.3-1) us  The chip shat having total varied unifor 55 Hz.  The frequent 10 Hz shall the motion shall	nip to the test sing eutectic Il be subjecte amplitude of rmly between cy ranging fr be traversed i be applied fo	solder. ed to a simpl 1.5mm, the the approx om 10 to 55 in approxima	s epoxy board shown le harmonic motion frequency being imate limits of 10 and Hz and returning to ately 1 minute. This of 2 hours in each 3 otal of 6 hours).
Dropping	No visible mechanical damage.			Drop chip induction of 100 cm.	ctor 10 times	on a concre	te floor from a height
Solderability	<ul><li>No visible mechanica</li><li>Wetting shall exceed</li></ul>	_	age.	<ul> <li>❖ Solder temperature: 245±2℃.</li> <li>❖ Duration: 3 sec.</li> <li>❖ Solder: Sn/3.0Ag/0.5Cu.</li> <li>❖ Flux: 25% Resin and 75% ethanol in weight.</li> </ul>			

<u> </u>	Specifications for C	inp i i C i iici iiiistoi
Resistance to Soldering Heat	<ul> <li>No visible mechanical damage.</li> <li>R25 change: within ±20%.</li> <li>R25 change: within ±30%.(103/473)</li> </ul>	<ul> <li>❖ Solder temperature: 250±5℃</li> <li>❖ Duration: 5 sec.</li> <li>❖ Solder: Sn/3.0Ag/0.5Cu.</li> <li>❖ Flux: 25% Resin and 75% ethanol in weight.</li> <li>❖ The chip shall be stabilized at normal condition for 1~2hours before measuring.</li> </ul>
Thermal Shock	<ul> <li>No visible mechanical damage.</li> <li>R25 change: within ±20%.</li> <li>R25 change: within ±30%.(103/473)</li> </ul>	<ul> <li>Temperature, Time: -40°C for 30±3 min→ 125°C for 30±3min.</li> <li>Transforming interval: 5sec. Max.</li> <li>Tested cycle: 5 cycles.</li> <li>The chip shall be stabilized at normal condition for 1~2 hours before measuring.</li> </ul>
Resistance to Low Temperature	<ul> <li>No visible mechanical damage.</li> <li>R25 change: within ±20%.</li> <li>R25 change: within ±30%.(103/473)</li> </ul>	<ul> <li>❖ Temperature: -40±3℃</li> <li>❖ Duration: 1000+24 hours.</li> <li>❖ The chip shall be stabilized at normal condition for 1~2 hours before measuring.</li> </ul>
Resistance to High Temperature	<ul> <li>No visible mechanical damage.</li> <li>R25 change: within ±20%.</li> <li>R25 change: within ±30%.(103/473)</li> </ul>	<ul> <li>❖ Temperature: 125±3℃</li> <li>❖ Duration: 1000+24 hours.</li> <li>❖ The chip shall be stabilized at normal condition for 1~2 hours before measuring.</li> </ul>
Damp Heat (Steady States)	<ul> <li>No visible mechanical damage.</li> <li>R25 change: within ±10%.</li> <li>R25 change: within ±30%.(103/473)</li> </ul>	<ul> <li>❖ Temperature: 60±2℃</li> <li>❖ Humidity: 90% to 95% RH.</li> <li>❖ Duration: 1000+24 hours.</li> <li>❖ The chip shall be stabilized at normal condition for 1~2 hours before measuring.</li> </ul>
Loading at High Temperature (Life Test)	<ul> <li>No visible mechanical damage.</li> <li>R25 change: Within ±20%.</li> <li>R25 change: within ±30%.(103/473)</li> </ul>	<ul> <li>❖ Temperature: 85±2℃</li> <li>❖ Duration: 1000+24 hours.</li> <li>❖ Applied current: Max. Permissive Operating Current.</li> <li>❖ The chip shall be stabilized at normal condition for 1~2 hours before measuring.</li> </ul>
Climatic sequence test	<ul> <li>No visible mechanical damage.</li> <li>R25 change: within ±20%.</li> <li>R25 change: within ±30%.(103/473)</li> </ul>	<ul> <li>Temperature, Time: 125°C for 16 hours</li> <li>First cycle: 40 °C 95%RH x 24 hours</li> <li>-40°C,2 hours</li> <li>Five cycles 40°C 95% RH x 24 hours/time</li> </ul>

## Packaging

## (1) Figure



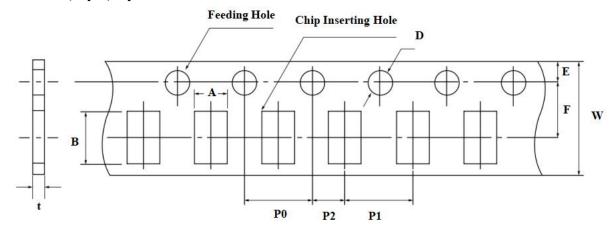


### (2) Quantity

Size(EIA)		0402	0603	0805	
Taping Type		PAPER	PAPER	PAPER	
	Reel	10K	4K	4K	
Quantity	Inner Box	10K×10=100K	4K×10=40K	4K×10=40K	
	Outer Box	10K×10×6=600K	4K×10×6=240K	4K×10×6=240K	

## (3) Tape Size

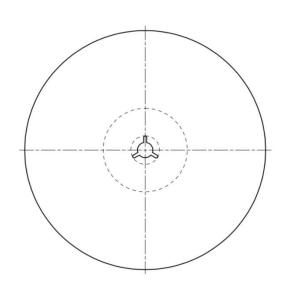
## Cardboard(Paper) tape

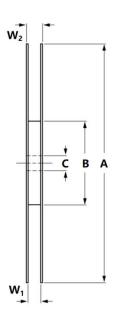


Unit: mm

Size (EIA)	A	В	W	F	E	P1	P2	P0	D	t
0402	0.65±0.1	1.15±0.1				2.00 ±0.05				≤0.8
0603	1.0±0.2	1.8±0.2	8.00 ±0.30	3.50 ±0.05	1.75 ±0.10	4.00 ±0.10	2.00 ±0.05	4.00 ±0.10	Φ 1.50 +0.1/-0.03	≤1.1
0805	1.5±0.2	2.3±0.2				4.00 ±0.10				≤1.1

### (4) Reel Size



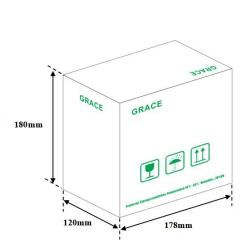


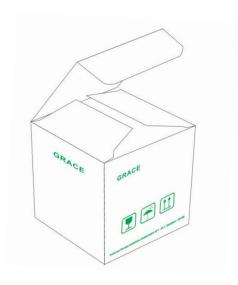
Type	Symbol	Dimensions(mm)	
	A	178±2	
	В	58±2	
7" Reel	C	13.5±0.2	
	W1	8.4+1.5/-0.0	
	W2	≤14.4	

#### (5) BOX package

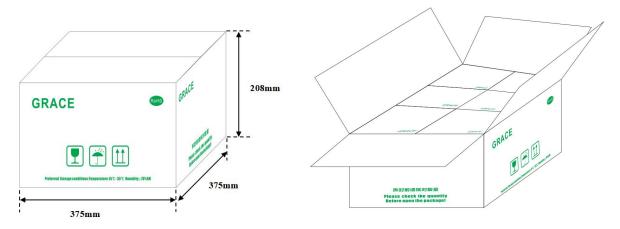
Double packaging with the paper type of inner box and outer box.

#### Inner Box :





#### Outer Box:



**\*** Box size specifications for reference.

### Storage environment

#### (1) Recommendation for temperature/humidity

- ❖ Even taping and packaging materials are designed to endure a long-term storage, they should be stored with a temperature of -10~40 ℃ and an RH of 0~70% otherwise, too high temperatures or humidity may deteriorate the quality of the chip rapidly.
- Packaging material may be deform-ed if package are stored where they are exposed to heat of direct sunlight.
- ❖ As oxidization is accelerated when relative humidity is above 70%RH, the lower the humidity is, the better the solderability is.
- As the temperature difference may cause dew condensation during the storage of the chip, it is a must to maintain a temperature control environment.

#### (2) Shelf Life

- An allowable storage period should be within 12 months from the outgoing date of delivery in consideration of solderability.
- ❖ As for chips in storage over 12 months, please check solderability before use.

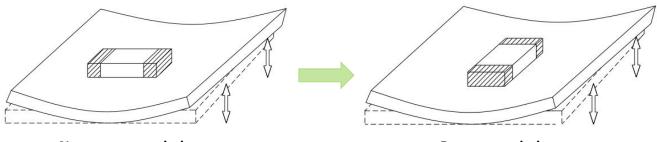
#### (3) Caution for corrosive environment

As corrosive gases may deteriorate the solderability of chip outer termination, it is a must to store chip in an environment without gases, chip that is exposed to corrosive gases may cause its quality issues due to the corrosion of plating layers and the penetration of moisture

### Process of Mounting and Soldering

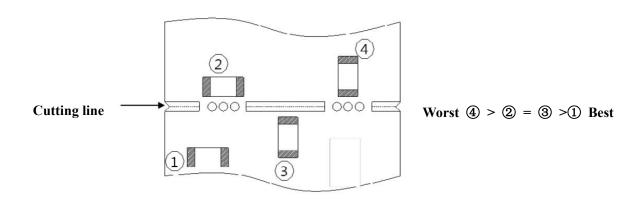
#### (1) Mounting

It is recommended to locate the major axis of chip in parallel to the direction in which the stress is applied.

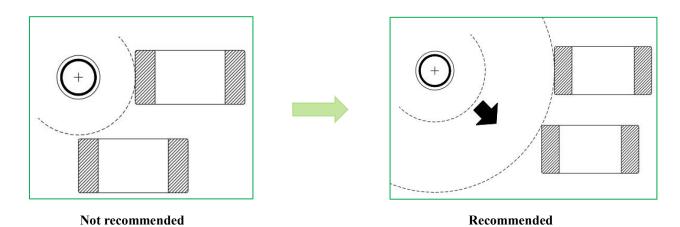


Not recommended Recommended

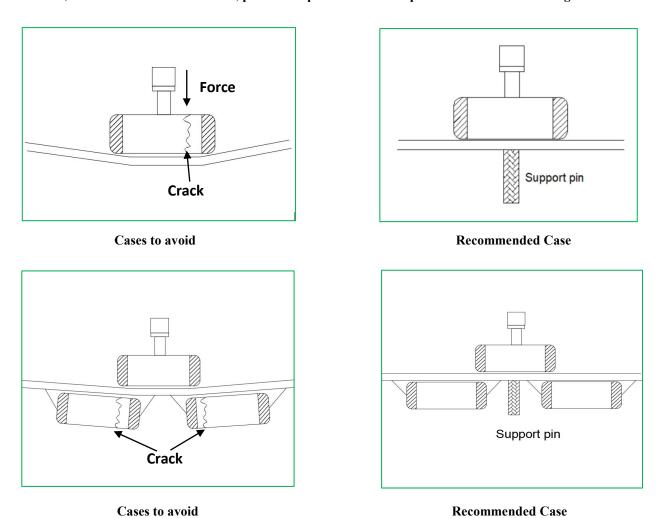
Please take the following measures to effectively reduce the stress generated from the cutting of PCB. Select the mounting location shown below, since the mechanical stress is affected by a location and a direction of chip mounted near the cutting line.



If the chip is mounted near a screw hole, the board deflection may be occurred by screw torque. Mount the chip as far from the screw holes as possible.



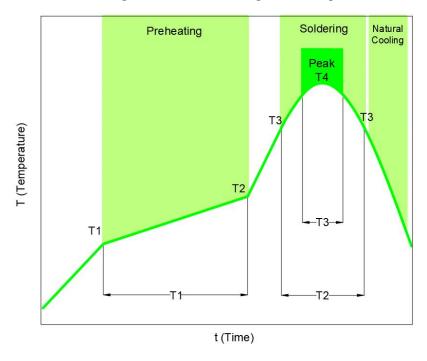
Substrate fixes up back surface of substrate with support pin in impact of suction nozzle to wely deflection to the utmost, and substrate hold deflection, please. A representative example is shown in the following.



We Dust accumulated in a suction nozzle and suction mechanism can impede a smooth movement of the nozzle. This may cause cracks in the chip due to the excessive force during mounting. If the mounting claw is worn out, it may cause cracks in the chip due to the uneven force during positioning. A regular inspection such as maintenance, monitor and replacement for the suction nozzle and mounting claw should be conducted.

#### (2) Reflow soldering

The reflow soldering temperature conditions are composed of temperature curves of Preheating, Temp. rise, Heating, Peak and Gradual cooling. Large temperature difference inside the chip caused by rapid heat application to the chip may lead to excessive thermal stresses, contributing to the thermal cracks. The Preheating temperature requires controlling with great care so that tombstone phenomenon may be prevented. Follow the recommended soldering conditions to avoid degradation of performance.



**	Specification		
Item	For eutectic mixture solder	For lead-free solder	
Preheating temperature	160 ∼ 180 °C	150 ∼ 180 °C	
Solder melting temperature	200 ℃	230 ℃	
Maximum temperature	240° C max.	260 °C max.	
Preheating time	100s max.	120s max.	
Time to reach higher than the solder melting temperature	30s max.	40s max.	
number of possible reflow cycles	2 max.	2 max.	

- **\*\*** Pre-heating is necessary for all constituents including the PCB to prevent the mechanical damages on the chip. The temperature difference between the PCB and the component surface must be kept to the minimum.
  - a. Allowable temperature difference  $\triangle T \le 150$  °C
  - b. Use non-activated flux. (Max. Cl content less than 0.1%)

#### (3) Soldering Iron

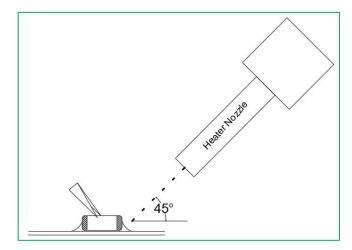
Manual soldering can pose a great risk on creating thermal cracks in the chip. The high temperature soldering iron tip may come into a direct contact with the ceramic body of the chip due to the carelessness of an operator. Therefore, the soldering iron must be handled carefully, and close attention must be paid to the selection of the soldering iron tip and to temperature control of the tip.

Iron soldering power	ron soldering power Soldering time Soldering Temp.		Number of times	Pre-heating	
2011	2	200 + 100 C	Within each terminal	① ΔT≤130	
20W max.	3s max.	300±10°C max.	once(Within total of twice)	② ≥60S	

- \* Keep the contact time between the outer termination of the chip and the soldering iron as short as possible. Long soldering time may cause problems such as adhesion deterioration by the leaching phenomenon of the outer termination.
  - a. Control  $\Delta$  T in the solder iron and preheating temperature;
  - b. Caution Iron tip should not contact with ceramic body directly;
  - c. Do not cool down the chip and PCB rapidly after soldering;
  - d. Lead-free solder: Sn-3.0Ag-0.5CU.

#### (4) Spot heater

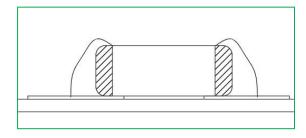
Compared to local heating with a soldering iron, hot air heating by a spot heater heats the overall component and board, therefore, it tends to lessen the thermal shock. In the case of a high density mounted board, a spot heater can also prevent concerns of the soldering iron making direct contact with the component.

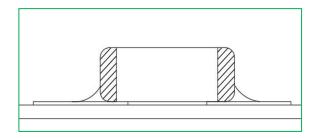


Distance	Hot Air Application angle	Hot Air Temperature Nozzle Outlet	Application Time
≥ 5mm	45°C	≤ 400°C	≤ 10s

**<sup>\*\*</sup>** If the distance from the air nozzle outlet to the chip is too close, the chip may be cracked due to the thermal stress.

#### (5) Recommended Amount of Solder





**Excessive amount** 

**Insufficient amount** 

#### **X** Notes:

- a. Too much solder amount will increase the risk of PCB bending or cause other damages.
- b. Too little solder amount will result in the chip breaking loose from the PCB due to the inadequate adhesive strength.
- c. Check if the solder has been applied properly and ensure the solder fillet has a proper shape.

#### (6) Cleaning

❖ In general, cleaning is unnecessary if rosin flux is used.

When acidic flux is used strongly, chlorine in the flux may dissolve into some types of cleaning fluids, thereby affecting the performance of the chip.

This means that the cleansing solution must be carefully selected and should always be new.

#### Cautions for cleaning

The chip or solder joint may be cracked with the vibration of PCB, if ultrasonic vibration is too strong during cleaning. Therefore, test should be done for the cleaning equipment and its process before the cleaning in order to avoid damages on the chip, you can refer to the following conditions for cleaning.

Ultrasound output	Ultrasound frequency	Cleaning time
20W/liter or less	40kHz or less	5minutes or less

## $\wedge$

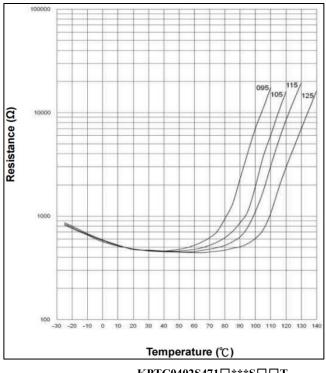
#### Limitation

Please contact us with usage environment information such as voltage, current, temperature, or other special conditions before using our products for the applications listed below. The products are not designed or warranted to meet the requirements of the applications listed below, whose performance and/or quality require especially high reliability, or whose failure, malfunction or trouble might directly cause damage to society, person, or property. Please understand that we are not responsible for any damage or liability caused by use of the products in any of the applications below.

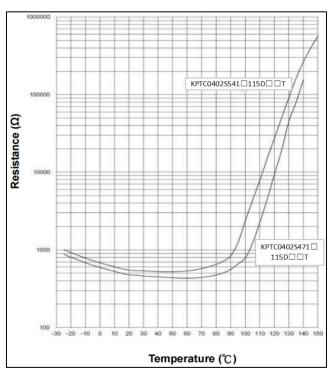
If you have any questions regarding this 'Limitation', you should first contact our sales personnel or application engineers.

- Aerospace/Aviation equipment1wheeler, 2wheeler and 3wheeler vehicle
- **Automotive of Transportation equipment**
- Military equipment
- Atomic energy-related equipment
- **Undersea equipment**
- **Medical equipment**
- Disaster prevention/crime prevention equipment
- Power plant control equipment
- Traffic signal equipment
- Data-processing equipment
- Electric heating apparatus, burning equipment
- **Safety equipment**
- Any other applications with the same as or similar complexity or reliability to the applications

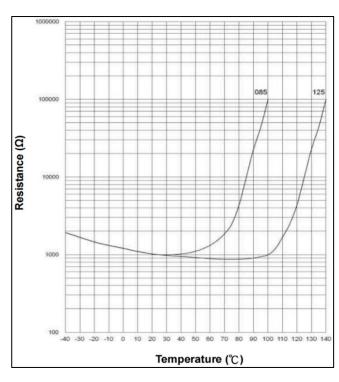
## **Resistance-Temperature Characteristics (Typical)**



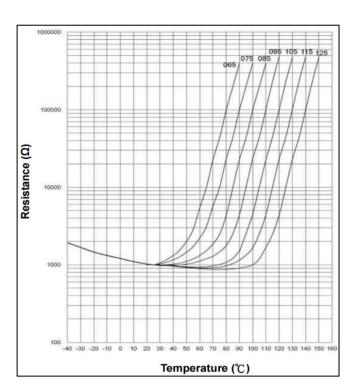
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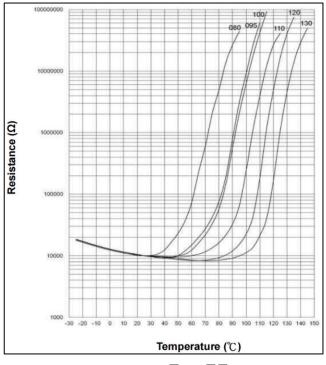
KPTC0402S471□115D□□T and KPTC0402S541□115D□□T



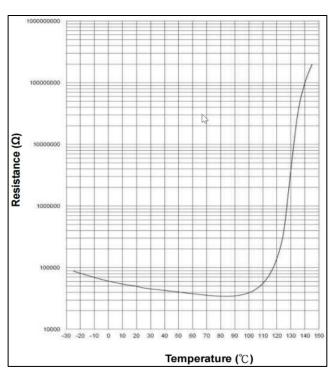
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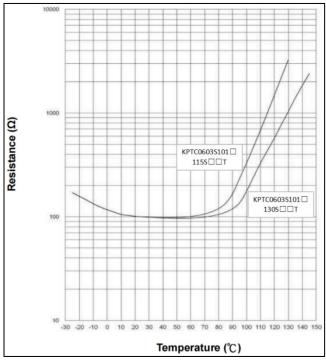
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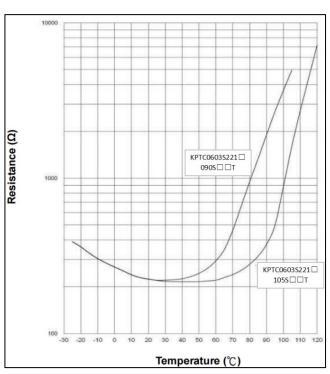




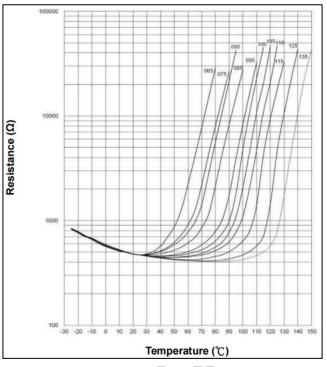
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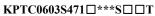


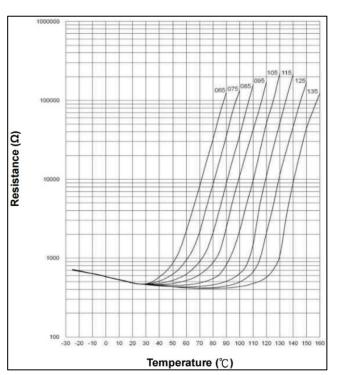
**KPTC0603S101□\*\*\*S□□T** 



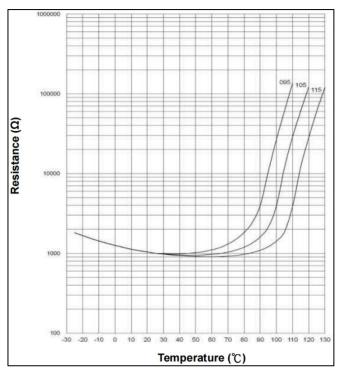
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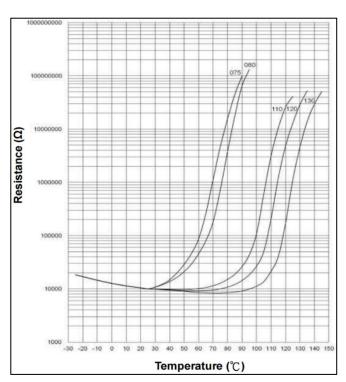




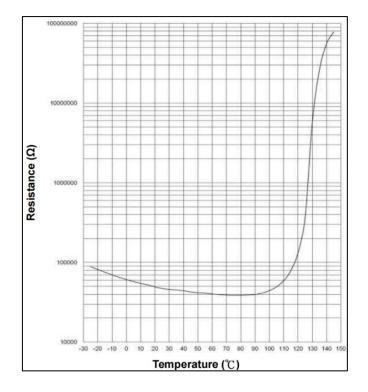
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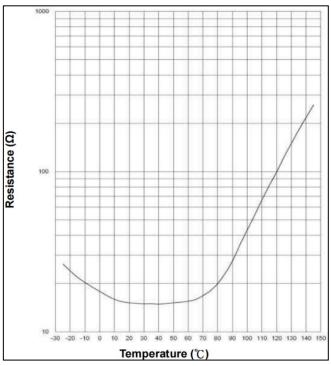


**KPTC0603S102□\*\*\*S□□T** 



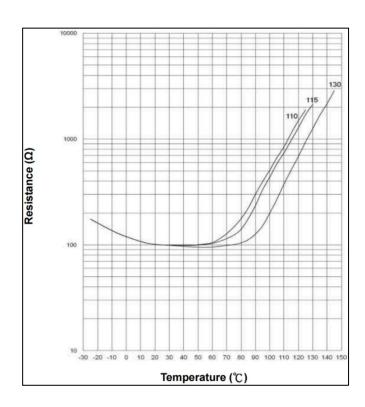
 $KPTC0603S103\,\square^{***}S\,\square\,\square T$ 

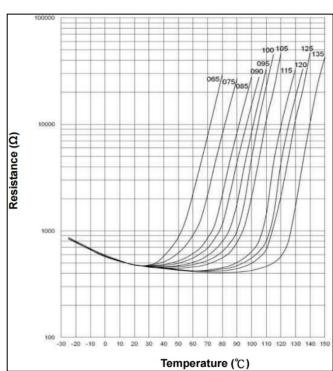




**KPTC0603S473**□130**S**□□**T** 

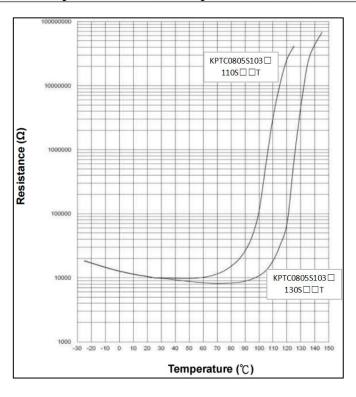






**KPTC0805S101□\*\*\*S□□T** 

**KPTC0805S471□\*\*\*S□□T** 



KPTC0805S103 = \*\*\*